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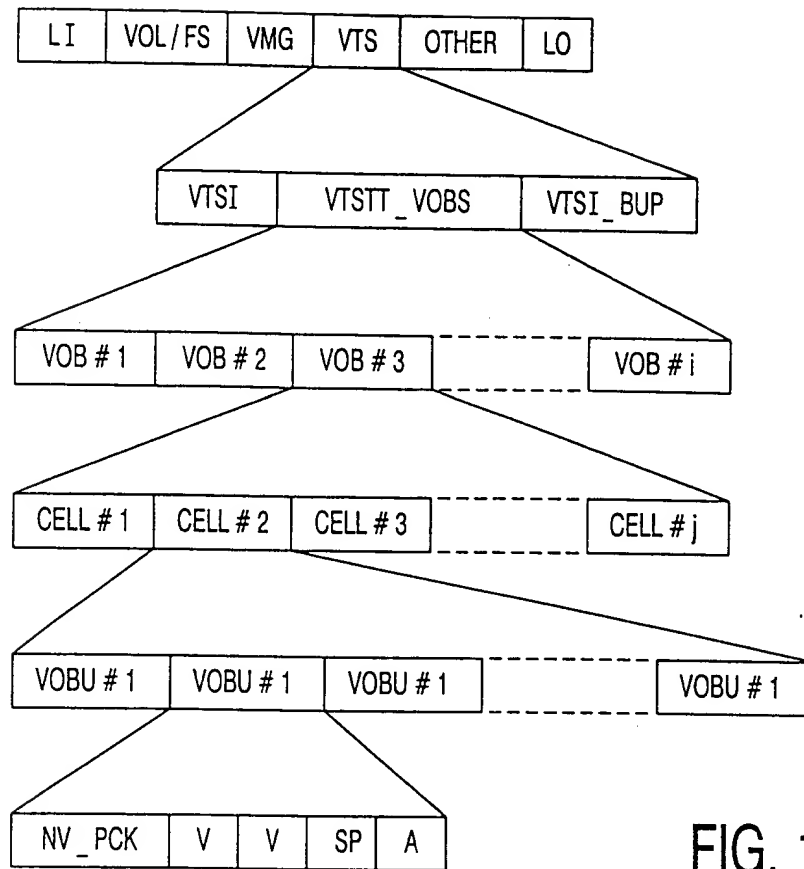


FIG. 1

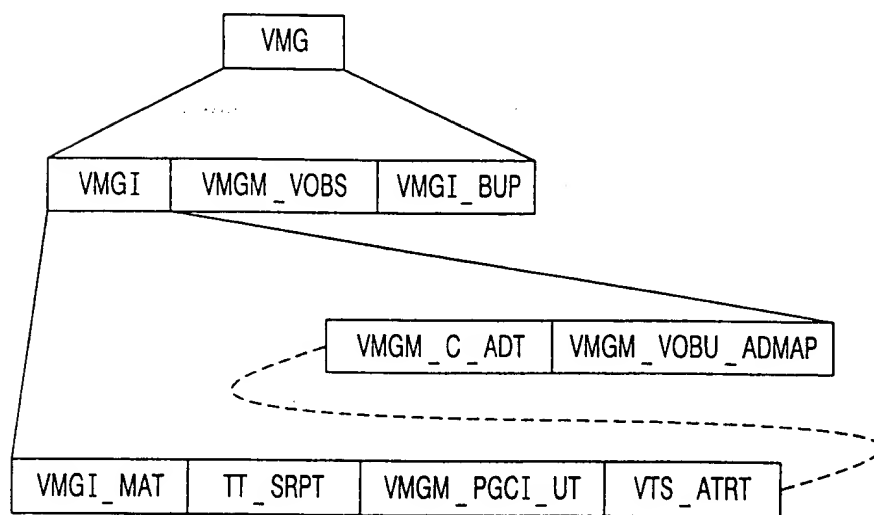


FIG. 2

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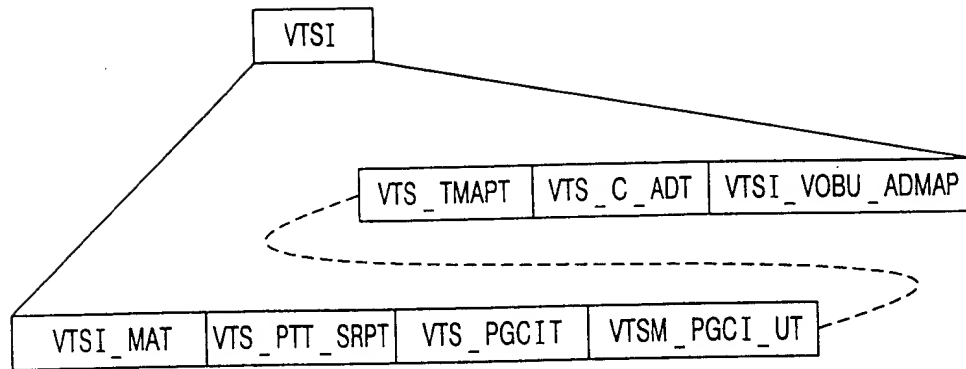


FIG. 3

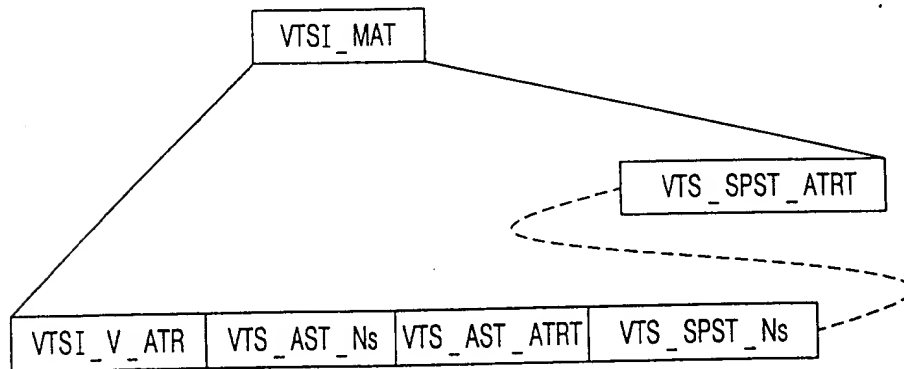


FIG. 4

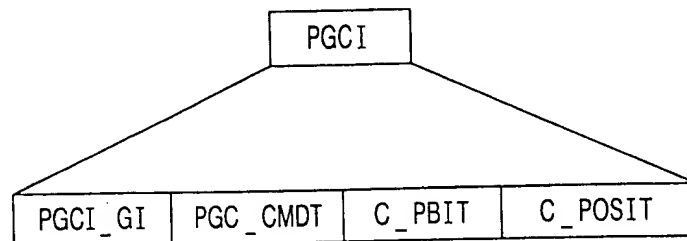


FIG. 5

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		VMGI/TT_SRPT/TT_SRP			VTSI/ VTS_PTT_S
		TT_PB_TY	PTT_Ns	VTS_TTN	
PLAY LISTS	EXAMPLE				
	PLAY LIST 1	0000 01x1		1	
	PLAY LIST 2			2	
				..	
	AS ORIGINAL I	0000 0101	4	I	①
				..	
	PLAY LIST K	0000 0101	3	K	②
				..	
	FREE SPACE	0000 0111	1	M	③
				..	
	PLAY LIST N	0001 01x1		N (< 50)	
ORIGINALS	ORIGINAL 1	0000 01x1		N + 1	
	ORIGINAL 2			N + 2	
				..	
	ORIGINAL I	0000 0101	4	N + I	④
				..	
	ORIGINAL K	0000 0101	5	N + K	⑤
				..	
	FREE SPACE	0000 0111	1	N + M	⑥
				..	
	ORIGINAL N	0001 01x1		N + N	

AGL\_Ns == '1'

TT\_PTL\_ID\_FLD == zero

VTSN == '1'

Bit 1 of TT\_PB\_TY indicates free space

FIG. 6A

VTSI/VTS_PTT_SRPT/TTUs	
PGCN	PGN
1	1
	..
2	1
..	..
I	1
	2
	3
	4
K	1
	2
	3
M	1
N	1
N	..
N + 1	1
	..
N + 2	1
..	..
N + I	1
	2
	3
	4
N + K	1
	2
	3
	4
	5
N + M	1
N + N	1
	..

FIG. 6B

VTSI/VTS_PGCIT/VTS_PGCI_SRP's	
VTS_TTN	VTS_PGCI_SA
1	
2	
...	
I	→ ORIGINAL I PGCI
...	
K	→ PLAYLIST K PGCI
...	
M	→ FREE SPACE M PGCI
...	
N	
N + 1	→ ORIGINAL 1 PGCI
N + 2	→ ORIGINAL 2 PGCI
...	
N + I	→ ORIGINAL I PGCI
...	
N + K	→ ORIGINAL K PGCI
...	
N + M	→ FREE SPACE M PGCI
...	
N + N	→ ORIGINAL N PGCI

Entry type == 1  
 Block mode == 00b  
 Block type == 00b  
 PTL\_ID\_FLD == 0000h

Equal VTS\_PGCI\_SA indicate "no THL"

FIG. 6C

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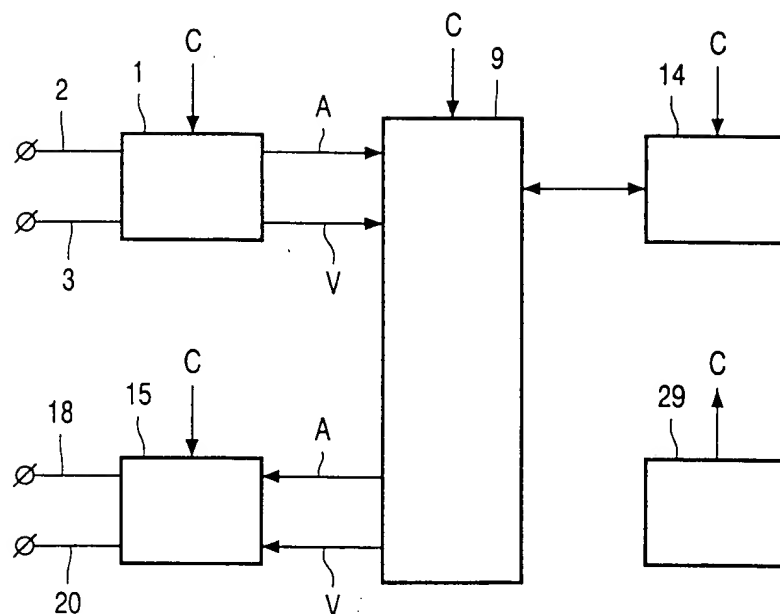


FIG. 7

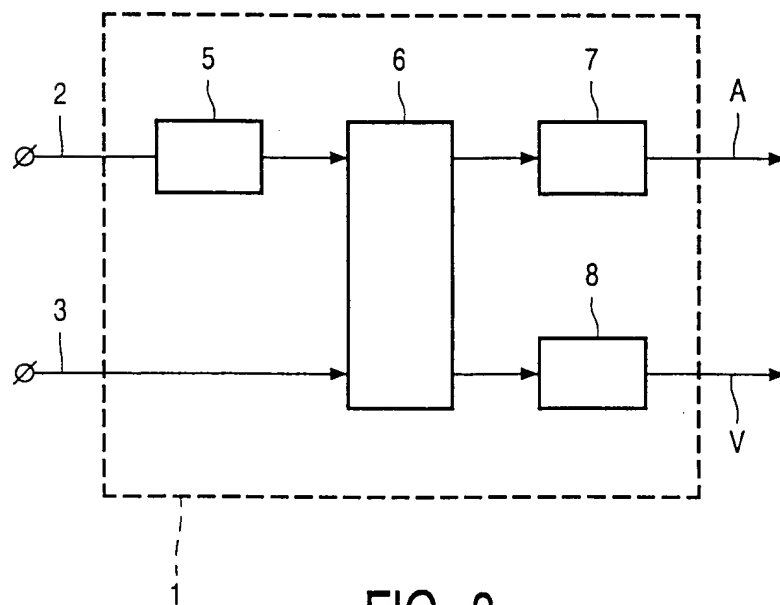


FIG. 8

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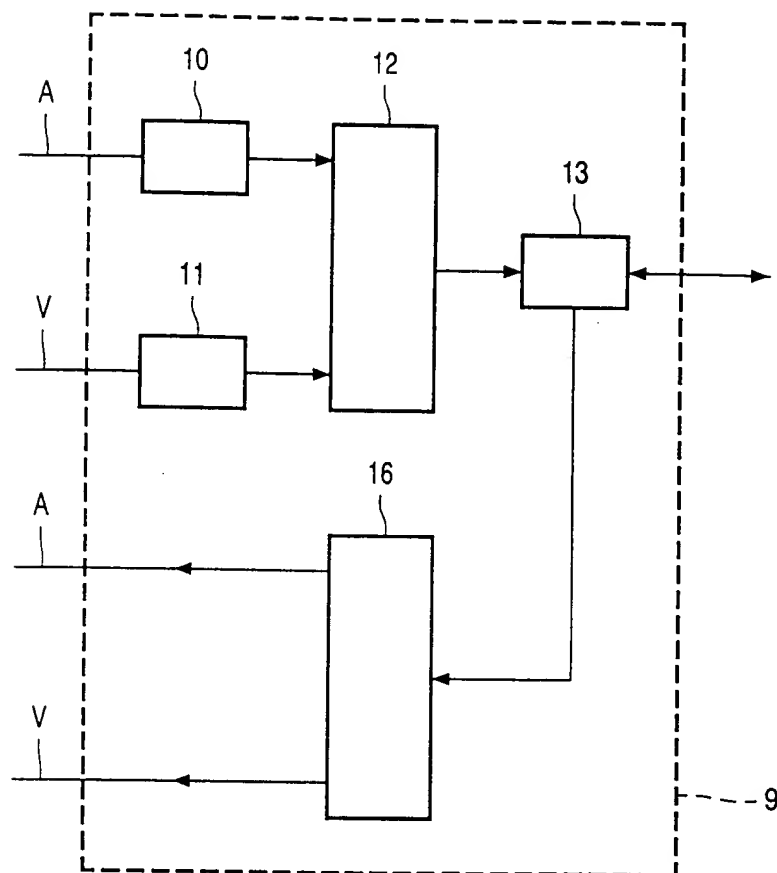


FIG. 9

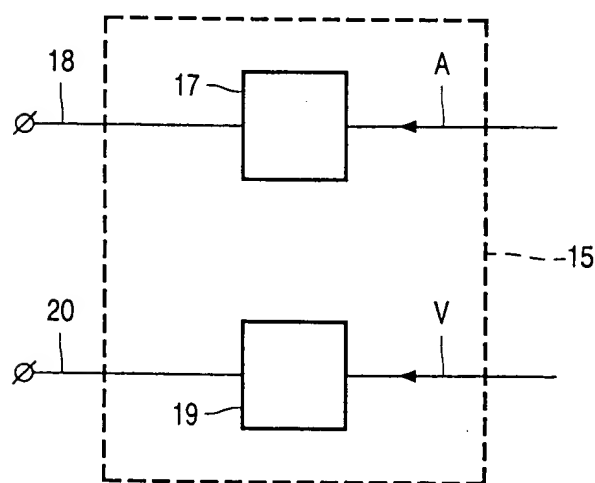


FIG. 10

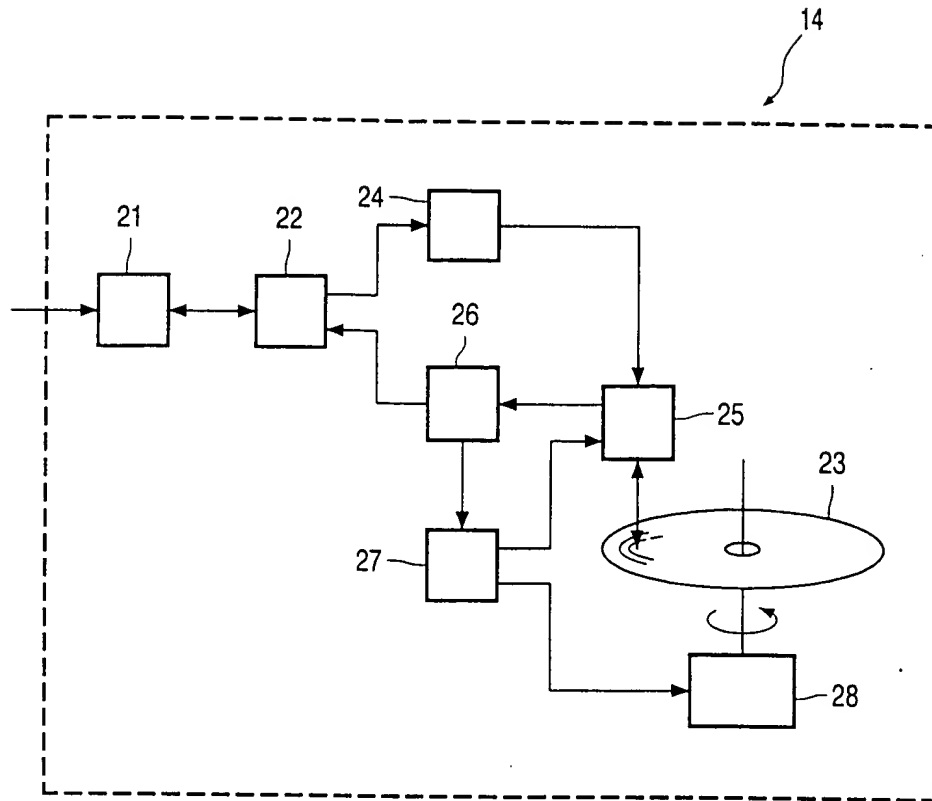


FIG. 11

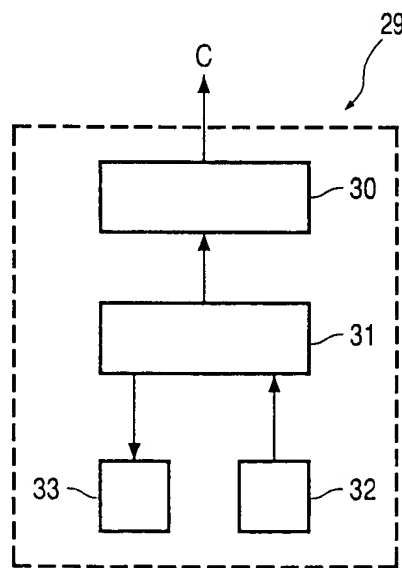


FIG. 12